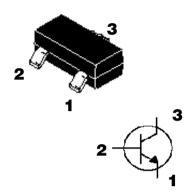
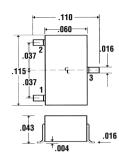


NPN General Purpose Transistor Mechanical Dimensions

FMBT3904





SOT-23

Dimensions in inches

Ratings	Symbol	Value	Units	
Collector - Emitter Voltage	V _{CEO}	40	Vdc	
Collector - Base Voltage	V _{CBO}	60	Vdc	
Emitter - Base Voltage	V_{EBO}	6.0	Vdc	
Collector Current (Continuous)	I _c	200	mAdc	
Thermal Characteristics	1	1		
Characteristic	Symbol	Max	Units	
Total Device Dissipation FR-5 Board (Note1) $T_A = 25^{\circ}C$	P _D	225	mW	
Derate above 25°C		1.8	mW/ºC	
Thermal Resistance	R _{eJA}	556	°C/W	
Total Device Dissipation Alumina Substrate, $T_A = 25^{\circ}C$ (Note 2)	P_{D}	300	mW	
Derate above 25°C		2.4	mW/°C	
Thermal Resistance	R _{eJA}	417	°C/W	
Junction and Storage Temperature	T_{J},T_{STG}	-55 to 150	°C	

Notes:

Maximum Patings

⁽¹⁾ FR-5 = 1.0 x 0.75 x 0.062 in.

⁽²⁾ Alumina = 0.4 x 0.3 x 0.024 in. 99.5% alumina.

⁽³⁾ Pulse test: Pulse width \leq 300 μ s, duty cycle \leq 2.0%.



FMBT3904 NPN General Purpose Transistor

Electrical Characteristics @ 25°C				
Off Characteristic Collector - Emitter Breakdown Voltage (Note 3) $(I_c = 1.0 \text{mAdc}, I_g = 0)$	Symbol V _{BR(CEO)}	Min 40	Max 	Unit Vdc
Collector - Base Breakdown Voltage $(I_c = 10\mu Adc, I_E = 0)$	V _{BR(CBO)}	60		Vdc
Emitter - Base Breakdown Voltage $(I_E = 10\mu Adc, I_C = 0)$	V _{BR(EBO)}	6.0		Vdc
Base Cutoff Current $(V_{CE} = 30Vdc, V_{EB} = 3.0Vdc)$	I _{BL}		50	nAdc
Collector Cutoff Current $(V_{CE} = 30Vdc, V_{EB} = 3.0Vdc)$	I _{CEX}		50	nAdc
n Characteristic	Symbol	Min	Max	Unit
DC Current Gain $(I_{c} = 0.1 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc})$ $(I_{c} = 1.0 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc})$ $(I_{c} = 1.0 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc})$ $(I_{c} = 10 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc})$ $(I_{c} = 50 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc})$ $(I_{c} = 100 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc})$	H _{FE}	40 70 100 60 30	 300 	
Collector - Emitter Saturation Voltage (Note 3) ($I_c = 10 \text{ mAdc}$, $I_B = 1.0 \text{ mAdc}$) ($I_c = 50 \text{ mAdc}$, $I_B = 5.0 \text{ mAdc}$)	V _{CE(sat)}		0.2 0.3	Vdc
Base - Emitter Saturation Voltage (Note 3) ($I_{\rm C}=10$ mAdc, $I_{\rm B}=1.0$ mAdc) ($I_{\rm C}=50$ mAdc, $I_{\rm B}=5.0$ mAdc)	V _{BE(sat)}	0.65	0.85 0.95	Vdc
mall-Signal Characteristic Current - Gain - Bandwidth Product ($I_c = 10 \text{ mAdc}, V_{ce} = 20 \text{ Vdc}, f = 100 \text{ MHz}$)	f _T	300		MHz
Output Capacitance $(V_{CB} = 5.0 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz})$	C _{obo}		4.0	pF
Input Capacitance ($V_{EB} = 0.5 \text{ Vdc}$, $I_{C} = 0$, $f = 1.0 \text{ MHz}$)	C _{ibo}		8.0	pF
Input Impedance $(V_{CE} = 10 \text{ Vdc}, I_C = 1.0 \text{ mAdc}, f = 1.0 \text{ kHz})$	h _{ie}	1.0	10	kΩ
Voltage Feedback Ratio $(V_{CE} = 10 \text{ Vdc}, I_{C} = 1.0 \text{ mAdc}, f = 1.0 \text{ kHz})$	h _{re}	0.5	8.0	x10 ⁻⁴
Small - Signal Current Gain $(V_{CE} = 10 \text{ Vdc}, I_{C} = 1.0 \text{ mAdc}, f = 1.0 \text{ kHz})$	h _{fe}	100	400	
Output Admittance $(V_{CE} = 10 \text{ Vdc}, I_{C} = 1.0 \text{ mAdc}, f = 1.0 \text{ kHz})$	h _{oe}	1.0	40	μmhos
Noise Figure (V $_{\text{CE}}$ = 5.0 Vdc, I $_{\text{C}}$ = 100 $\mu\text{Adc},\ R_{\text{S}}$ = 1.0 k $\Omega,\ f$ = 1.0 kHz)	NF		5.0	dB
vitching Characteristic				
Delay Time $(V_{CC} = 3.0 \text{ Vdc}, V_{BE} = 0.5 \text{ Vdc},$ Rise Time $I_{C} = -10 \text{ mAdc}, I_{B1} = 1.0 \text{ mAdc})$	t _d t _r		35 35	ns
$ \begin{array}{lll} \text{Storage Time} & & \text{(V_{CC} = 3.0 Vdc, I_{C} = 10 mAdc,} \\ \text{Fall Time} & & \text{I}_{\text{B1}}$ = I_{B2} = 1.0 mAdc) \\ \end{array} $	t _s		200 50	ns